



**Department of Electrical & Electronics Engineering**

**Webinar Report**

on

**“Career Opportunities after B.Tech and GATE Orientation”**

Organised by

**Department of Electrical and Electronics Engineering**

**09 -10-2021**

**Organized in association with: ISTE Students Chapter & OHM Institute, Hyderabad**

**Submitted by:** Dr Pratap Ranjan Mohanty, Associate Professor., Dept. of EEE

**Resource Person:** Mr. K N S Hemanth (Subject Expert at OHM Institute Hyderabad, AIR 4 in IES, State 5th in AP TRANSCO, Alumni of BITS)

**Attendance: 83 participants from different institutions (Offline: 52 students and 05 faculties, Online: 26)**

The session was started at 11 AM. The webinar was initiated by Dr. A V Pavan Kumar, HOD-EEE Department. The resource person **Mr. KN S Hemanth**, was introduced by Dr. Pratap Ranjan Mohanty, Assoc. Prof., Dept. of EEE. The resource expert addressed the career opportunities after B.Tech and importance of GATE.

At the beginning of the presentation, the eminent resource person rationalized everyone regarding the options after B.Tech in general. The pathway for the higher education is discussed with the significance of GATE, GRE, TOFEL and PG CET by individual state university. GATE is the gateway of many PSUs and Navratna Companies; mentioned during the session. Some of the Government examinations like BARC, ISRO, DRDO, NPCIL, BHEL, VIZAG Steel etc. have been conducting separately with predefined interval regularly. Syllabus of these examinations is aligned with GATE. Also, some of the government technical examinations like ESE, SSC JE, State SEBs, State PSC have been conducting in associate with GATE syllabus.

Besides, the pathway for MBA study (CAT, GMAT, XAT, GATE, ICET), Entrepreneurship/Start-ups, Civil Services/ Group A (UPSC Civil Service Examination, IFoS, State PSC for Group I services, SSC CGL for group B, C post), Defence (CDS, UES, AFCAT, TES), Social & Environmental Sector (TISS Mumbai/ HYD, TERI Delhi, IIM Bangalore, ISFB- Delhi, ICSSR) is being highlighted during the session.

The prominent speaker briefed regarding the conduction of GATE examination in every years. The pattern of the GATE examination including mark distribution of each section (General Aptitude, Technical Subjects), duration and total number of questions, types of questions (MCQ, NAT, MSQ) were discussed. The speaker made an awareness on GATE and focused to improve the ability, determination and confidence of participants by discussing certain procedures while attending the examination. The session also pointed certain success tips to top GATE at the end of the presentation.

**Feedback:**

The participants were moreover passionate to know regarding the options after B.Tech in general and the pathway for the higher education/GATE examination. The participants were



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involved to understand the career opportunities after B.Tech and importance of GATE. The session was concluded followed by a vote of thanks, which was given by Dr. A V Pavan Kumar, Associate Professor & Head, Department of EEE.

**Participation Certificate:** Participation E-Certificates are distributed to all the active participants through their email.

### Photos:

**MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE**  
(UGC-AUTONOMOUS, Madanapalle, AP)

Webinar on  
**Career opportunities after B.Tech & GATE orientation**

ORGANISED BY  
**DEPARTMENT OF EEE**  
(Accredited by NBA)  
in Association with ISTE, MITS

IN ASSOCIATION WITH  
**OHM INSTITUTE HYDERABAD**

09-10-2021 11.00AM  
Platform: Microsoft Teams

By  
**K.N.S. HEMANTH**  
(Subject Expert at OHM, AIR 4 in IES, STATE 5<sup>th</sup> IN AP TRANSCO, Alumni of BITS)

<b>Faculty Coordinator</b> Dr. PRATAP RANJAN MOHANTY Assoc. Professor, Department of EEE	<b>Convener</b> Dr. A V PAVAN KUMAR Assoc. Professor & Head Department of EEE	<b>Patron</b> Dr. C. YUVARAJ Principal	<b>Chief Patron</b> Dr. H Vijaya Bhaskar Choudhary Ph.D Secretary & Correspondent
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**Success Tips to Top GATE**

**Subject Classification**

- Foundation Subjects (Pre-GATE)  
Ex: Network Theory, EME, Signals and Systems, Digital Electronics, Mathematics
- Cyclical Subjects (Repeated Exams)  
Ex: Power System, Microprocessor, Communications.
- Application Subjects  
Ex: Machines, Control Systems, Power Electronics, Analog Electronics, EDC, Measurements

**Success Tips to Top GATE**

**Personal Commitment**

- Dedicate time for study of concepts & practice
- No ups & downs in momentum
- Reduce distractions
- Periodic revision of completed subjects
- Maintaining own short notes



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Organizing Institutes

- (2020) IITD
- (2021) IITB
- (2014, 2022) IIT KGP
- (2019) IITM
- (2015) IITK
- (2018) IITG
- (2017) IITR
- (2016) IISc
- OHM

K H (Guest)

AS  
Adarsh S

HOD- E

KH  
K H (Guest)

Subject	Standard Textbook
Electric Circuits	Engineering Circuit Analysis by Hayt & Kemmerly (8 <sup>th</sup> edition)
Signals & Systems	Signals & Systems by Alan V. Oppenheim (2 <sup>nd</sup> edition)
Control Systems	Control Systems Engineering by Nagrath & Gopal (6 <sup>th</sup> edition)
Electromagnetics	Principles of Electromagnetics by Sadiku (6 <sup>th</sup> edition)
Analog Electronics	Microelectronic Circuits by Sedra Smith (7 <sup>th</sup> Edition)
Digital Electronics	Digital Design by Morris Mano (6 <sup>th</sup> edition)
Measurements	Electrical and Electronic Measurements by AK Sawhney
Electrical Machines	Electrical Machinery by PS Bimbhra (7 <sup>th</sup> edition)
Power Systems	Power System Engineering by Nagrath & Rathi (3 <sup>rd</sup> edition)
Power Electronics	Power Electronics by PS Bimbhra (6 <sup>th</sup> edition)
Engineering Mathematics	Engineering Mathematics by SS Grewal (44 <sup>th</sup> edition)
General Aptitude	Quantitative Aptitude and Verbal & Non-Verbal Reasoning by RS Agarwal

K H (Guest)

AS  
Adarsh S

HOD- E

KH  
K H (Guest)

### Sample e-certificate

**MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE**  
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Website: JNTUA, Anantapuramu & Approved by AICTE, New Delhi  
Madanapalle-517326, Chittoor Dist., Andhra Pradesh.  
www.mits.ac.in

**Certificate of Participation**

This is to certify that Dr./Mr./Mrs. TANMAYA PANDA  
of NATIONAL INSTITUTE OF SCIENCE AND TECHNOLOGY  
has attended a one day webinar on "Career Opportunities after B.Tech & GATE Orientation", Organized by Department of Electrical & Electronics Engineering, in association with OHM INSTITUTE, HYDERABAD & ISTE Students Chapter at Madanapalle Institute of Technology and Science, Madanapalle on 09<sup>th</sup> October 2021.

Coordinator  
H. O. D of EEE  
Principal

MITS/EE/WEB/001/2021/10/25



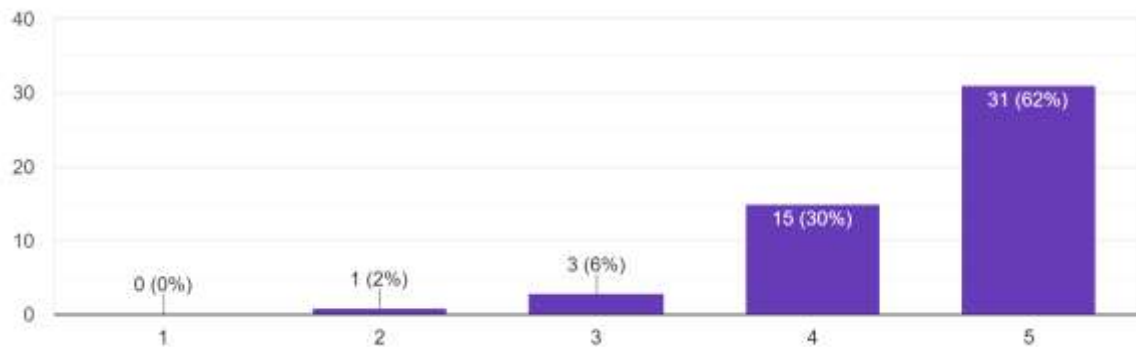
**Department of Electrical & Electronics Engineering**

**Feedback Analysis**

Link for feedback: <https://docs.google.com/forms/d/e/1FAIpQLSfalBpJ45AZ-kmaBilpTeNDIn5j0JB3l6FbBO12kbSjUx3pJQ/viewform>

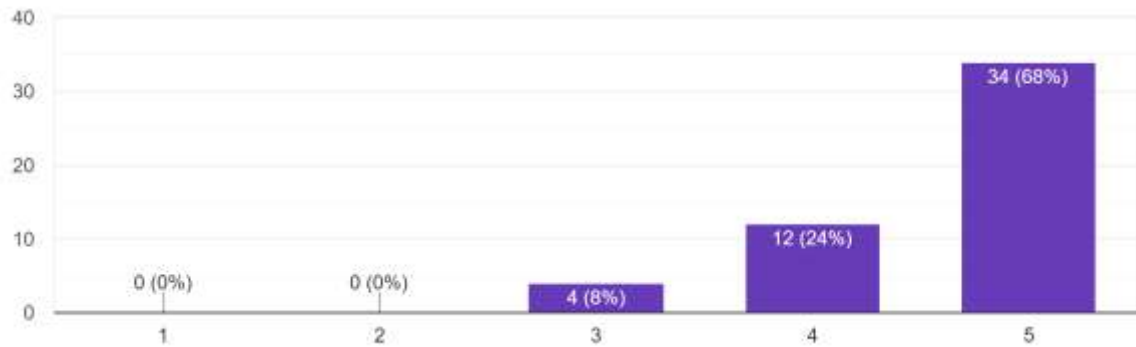
1. The interactive session was scheduled at a suitable time

50 responses



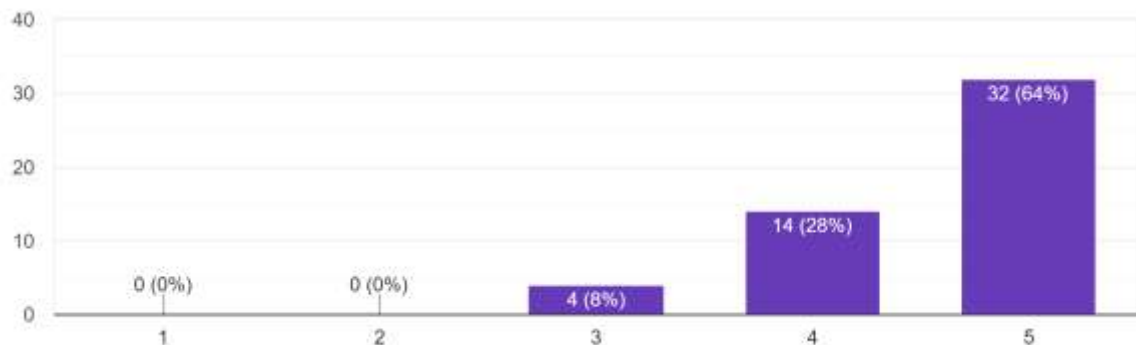
2. The interaction was useful and resource person explanation.

50 responses



3. The information in the interaction was presented in a clear and organized manner.

50 responses

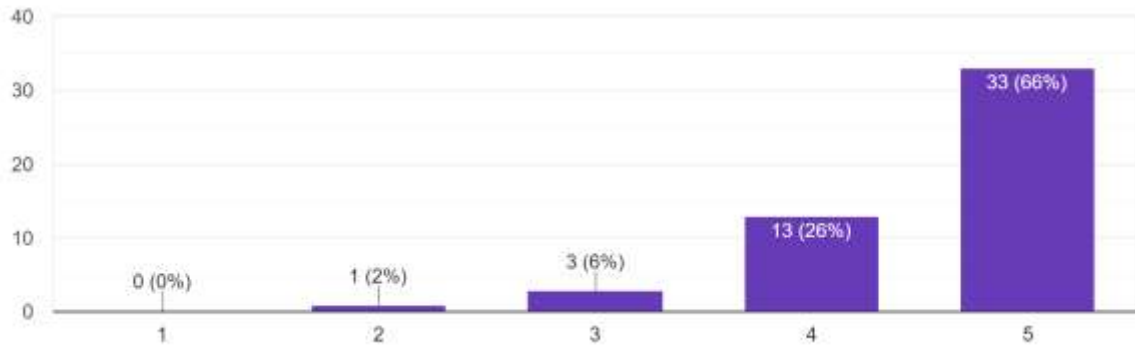




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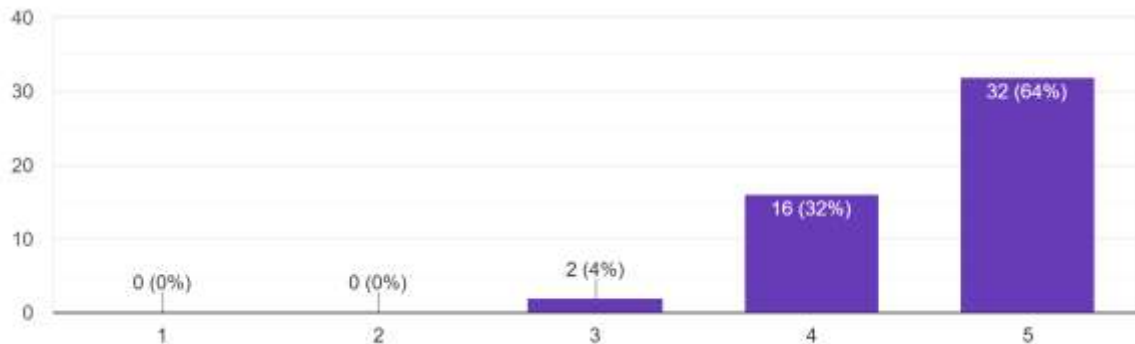
4. The presenter responded to questions in an informative, appropriate and satisfactory manner.

50 responses



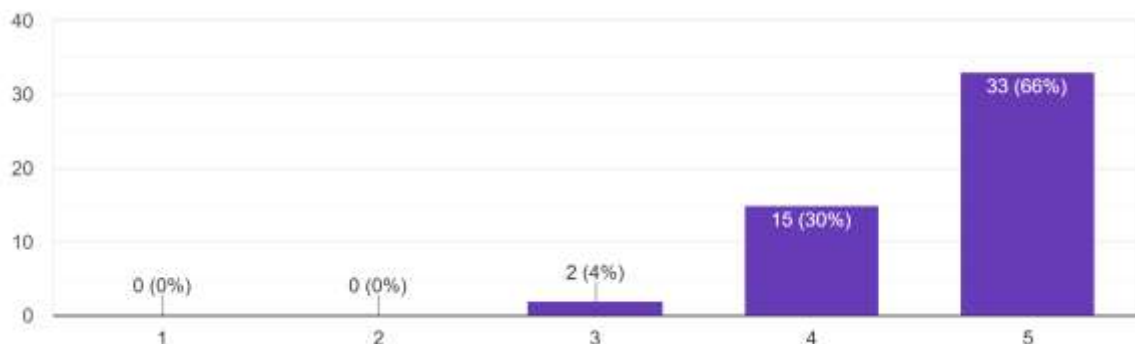
5. your impression of facilities provided by the institute for interaction.

50 responses



6. Overall, the session was informative and valuable.

50 responses

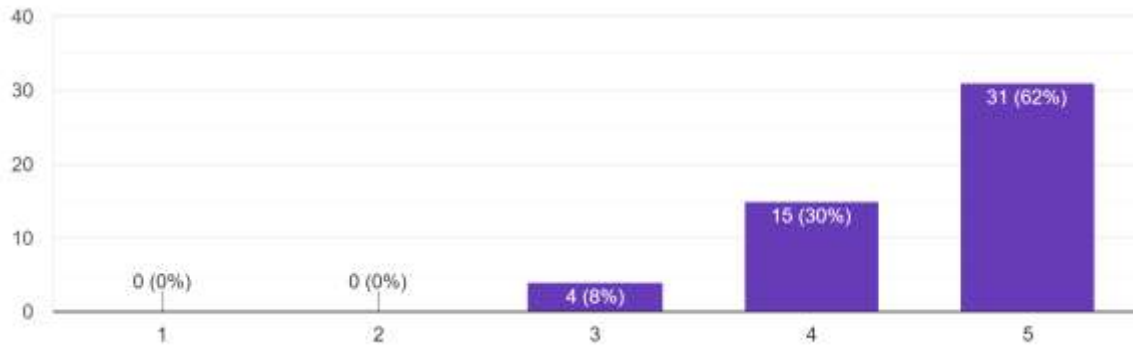




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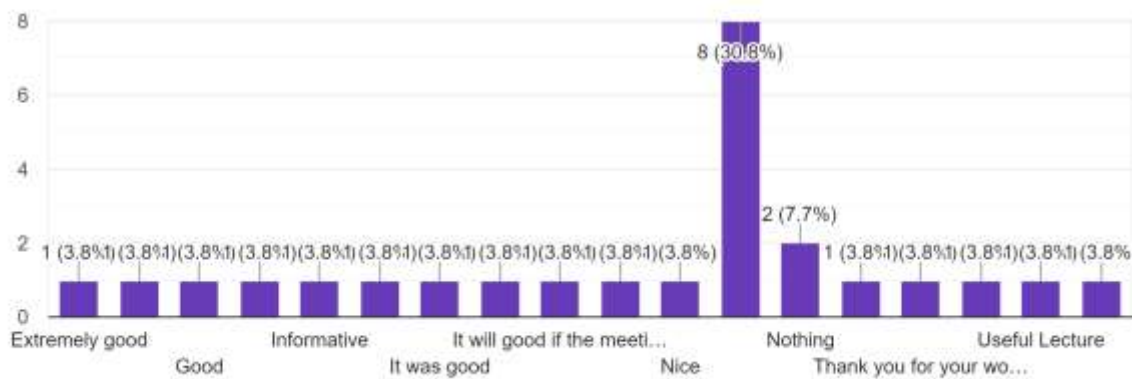
7. In what ways could this interaction have been improved to better suit your needs?

50 responses



8. Any Other Comments

26 responses



Signature of the Coordinator

Signature of HoD